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November 18, 2008

Patent No. : 7401204
Inventor : Hideo Miyake et al.
Patent Issued : July 15, 2008
Title : PARALLEL PROCESOR EFFICIENTLY EXECUTING VARIABLE INSTRUCTION
:WORD

Re: Request for Certificate of Correction

Consideration has been given your request for the issuance of a certificate of correction for the above-identified patent under the provisions of Rule(s) 1.322.

Inspection of the application for the patent reveals that in connection with the alleged error pertaining to column 26 and 27; the printed Letters Patent with regard to applicants request is considered in accordance with records within the Patent and Trademark Office. Accordingly, there being no fault on the part of the Patent and Trademark Office, it has no authority to issue a certificate of correction under the provisions of 35 U.S.C. 254 and Rule 322 of the Rules of Practice of the United States Patent and Trademark Office in Patent Cases.

In view of the foregoing applicants request in this matter is hereby denied. A certificate of correction will issue to correct the remaining error noted in applicants request.

Any telephone inquiry concerning this communication should be directed to Ms. A. Green at (703) 308-9380 ext. 123.

Mary Diggs, Supervisor
Decisions & Certificates
of Correction Branch
(703) 308-9390 or (703) 308-

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